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FIG. 1A

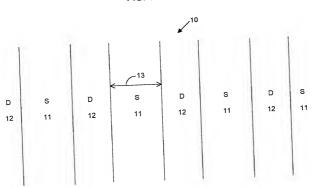
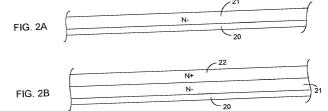


FIG. 1B



For : WAFER SURFACE THAT FACILITATES PARTICLE REMOVAL Sheet 2 of 4 CONDUCTOR 23 FIG. 2C N+ N-- 20 N+ CONDUCTOR FIG. 2D 23 N+ N-~ 20 ∠25A ≥ 25B N+ N+ CONDUCTOR CONDUCTOR N+ N+ N-20 FIG. 2E 26 < 25A ≥ 25B N+ N+ CONDUCTOR CONDUCTOR FILL FILL FILL N+ N+ N-~ 20 FIG. 2F -27A -28A -28B - 27B N+ N+ CONDUCTOR CONDUCTOR FILL FILL FILL N+ N+ N-N-~ 20 FIG. 2G

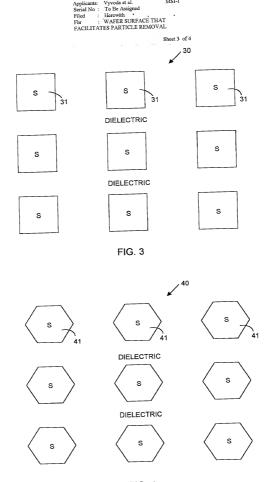
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FIG. 4

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Sheet 4 of 4

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FIG. 5